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ATM traffic generator card. An integrated solution

Hontas, S. Tselikis, G. Tompros, S. Giamniadakis, J. Andritsos, S. Loukatos, D. M. Dept. of Electr. & Comput. Eng., Inst. of Commun. & Comput. Syst., Athens, Greece;

This paper appears in: Computers and Communications, 1998. ISCC '98. Proceeding

Symposium on

Publication Date: 30 June-2 July 1998

On page(s): 161 - 165 Number of Pages: xvii+720

Meeting Date: 06/30/1998 - 07/02/1998

Location: Athens

INSPEC Accession Number:6117869

Digital Object Identifier: 10.1109/ISCC.1998.702477

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Abstract

In this paper the design and the implementation of an ATM traffic generator system is p principles of this document follow the architecture presented in Hontas et al. (1997). It is: with a careful allocation of functions between hardware and software, in a way that it can speed (155.52 Mbps) and, on the other hand, be flexible enough to emulate a wide range profiles (VBR and CBR). The system is composed of a basic traffic generator and ATM and four peripheral cards for the support of different physical interfaces. The architecture open to any future modifications concerning signalling and functionality. The software of t under Windows NT

Index Terms Inspec

Controlled Indexing

asynchronous transfer mode telecommunication computing telecommunication telecommunication signalling telecommunication traffic test equipment

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155.52 Mbit/s ATM signalling core card ATM traffic generator card CBR PC system VBR Windows NT software architecture constant bit rate periphera variable bit rate

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ATM burst traffic generator

Chu, P.P.

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Dept. of Electr. Eng., Cleveland State Univ., OH, USA;

This paper appears in: VLSI, 1995. Proceedings., Fifth Great Lakes Symposium on

Publication Date: 16-18 March 1995

On page(s): 262 - 265

Meeting Date: 03/16/1995 - 03/18/1995

Location: Buffalo, NY

INSPEC Accession Number:5143931

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Abstract

The Markov modulated process is widely used to model ATM (Asynchronous Transmissis sources because it can emulate the bursty and correlated **traffic** arrival. We propose a higenerator emulating the bursty behavior of the Markov process and develop circuits for a modulated process and an N-state homogeneous Markov modulated process. The generaffic imitating cell arrival of various voice and video sources. Our test circuit can be fitted sized FPGA chip and can generate up to 20M cell arrivals per second

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Markov processes asynchronous transfer mode field programmable gate array telecommunication traffic visual communication voice communication

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